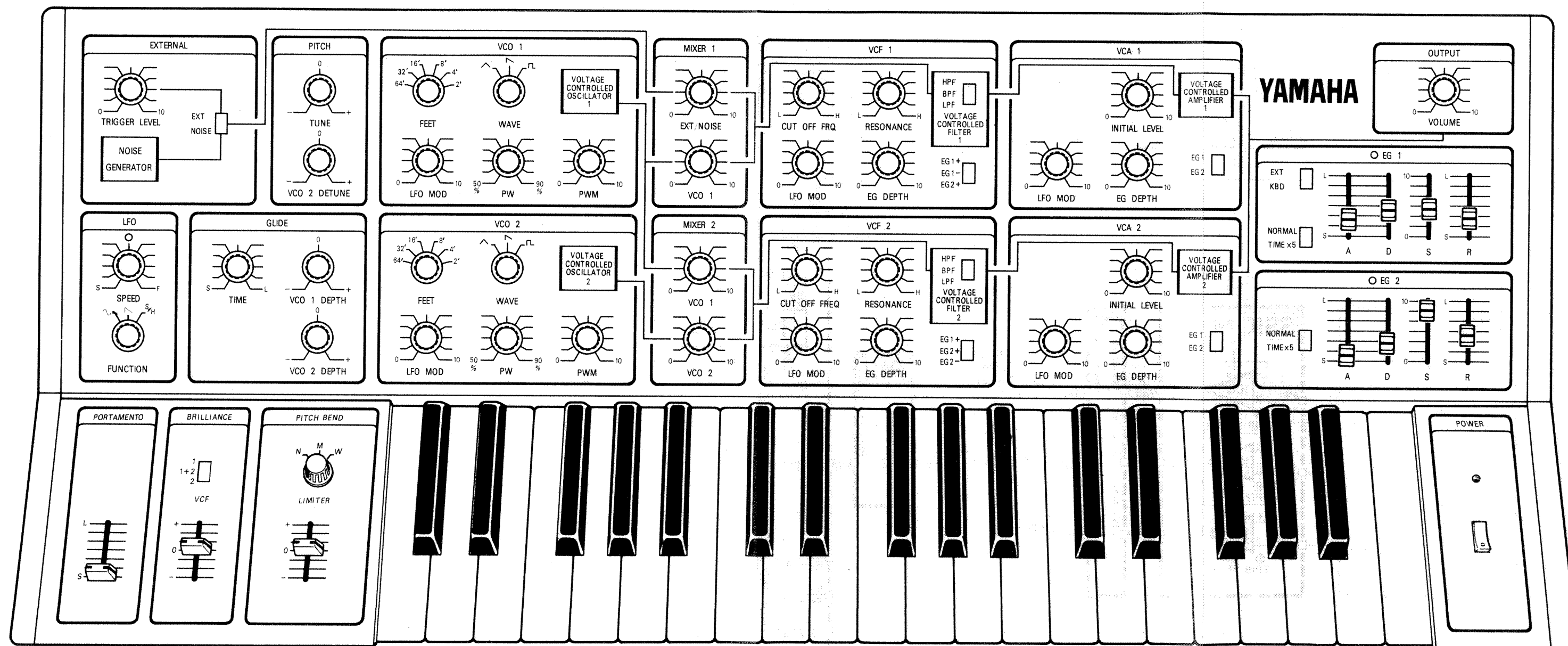
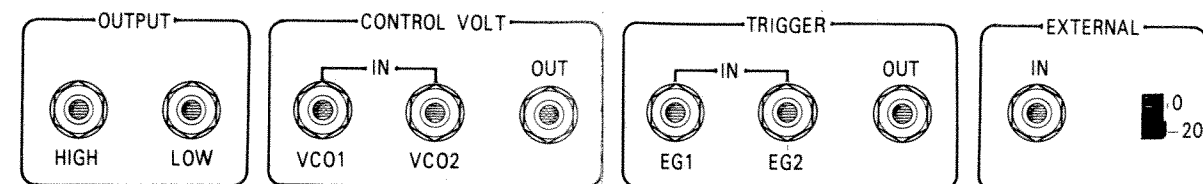
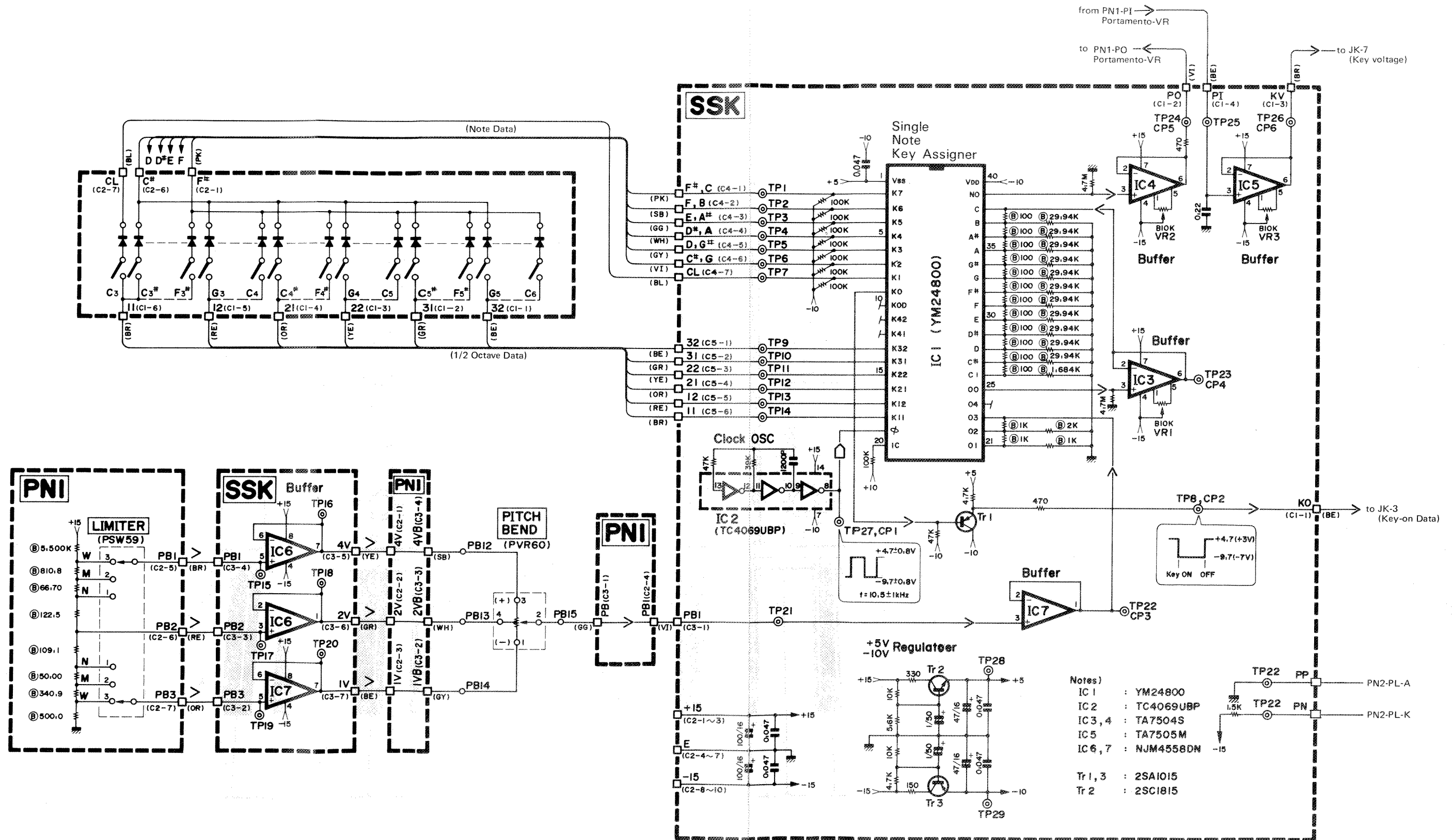


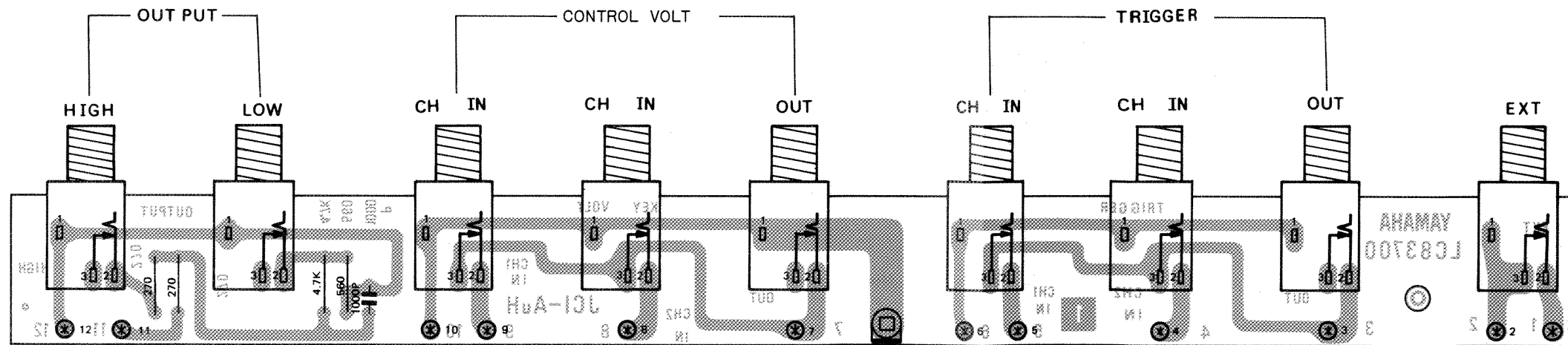
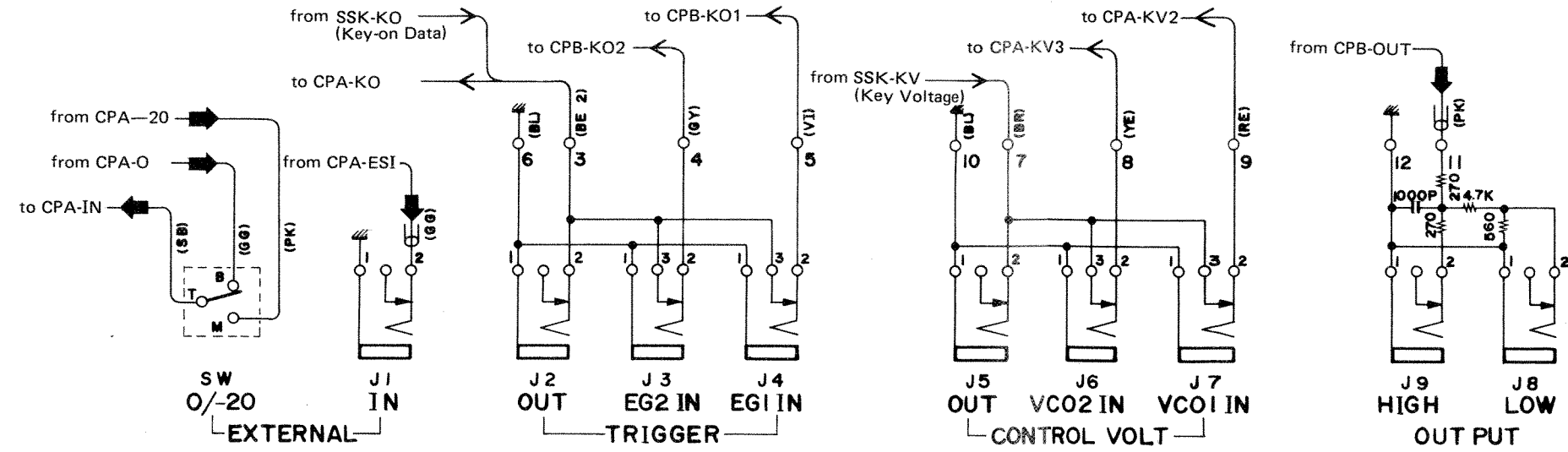
PANEL LAYOUT



SSK Circuit Diagram



REAR PANEL Circuit Diagram, Circuit Board



Note)
1. Printed Circuit Board LC83700

Electrical Checks & Adjustments

● REG CIRCUIT BOARD

1. ±15V Power Supply

- Adjust VR12 on the REG Circuit Board so as to read $+15 \pm 0.01V$ in between terminals "+15V" and "E" (TP1 and TP2 on the CPA board).
- Similarly adjust VR13 on the REG board so as to read $-15 \pm 0.01V$ in between terminals "-15V" and "E" (TP3 and TP2 on the CPA board).

● SSK CIRCUIT BOARD

1. +4.7V & -9.7V Power Supply

- Read $+4.7 \pm 0.8V$ at TP28.
- Read $-9.7 \pm 0.8V$ at TP29.

2. Clock Oscillator

- The waveform given below should be at TP27 (CP1).

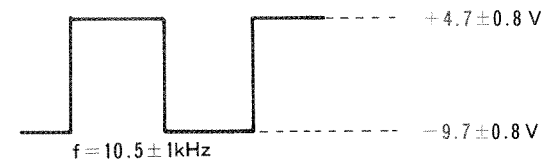


Fig. 1

3. Buffer Offset

- Set PORTAMENTO POT to S and turn on C3 key. Adjust VR1 so as to read $500 \pm 0.4mV$ at TR23 (CP4).
- In the same setting, adjust VR2 so as to read $250 \pm 0.4mV$ at TR24 (CP5).
- Similarly adjust VR3 so as to read $250 \pm 0.4mV$ at TR26 (CP6).
- Turn off C3 key and on C6 key, and read $2 \pm 0.001V$ at TP26 (CP6).

● CPA CIRCUIT BOARD

1. LFO

- Set SPEED control of LFO to S. Adjust VR3 to read $0.1 \pm 0.02Hz$ at TP11.
- Set SPEED control of LFO to F. Adjust VR2 to read $100 \pm 10Hz$ at TP11. A sawtooth wave should be at TP10.

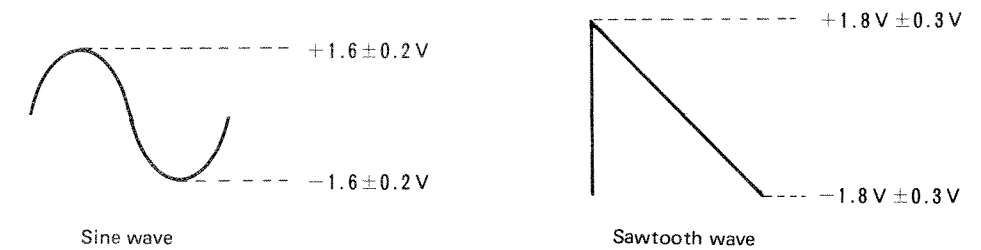


Fig. 2

2. EXTERNAL PREAMP and Trigger Circuit

- When 1kHz sine waves of $-30 \pm 1 dBm$ are applied to terminal EXTERNAL IN, output level should be $-10 \pm 3 dBm$ at TP5 and $10 \pm 3 dBm$ at TP16.
- The waveform given below should be at TP8 when TRIGGER LEVEL control is moved from 0 to 10 and back again to 0 while 1kHz sine waves of $+15 \pm 1 dBm$ are being applied to TP7.

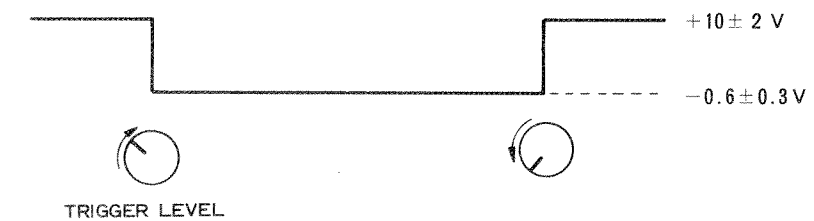


Fig. 3

3. Noise Generator and S/H Circuit

- Adjust VR1 so that the waveform given below be detected at TP9.



Fig. 4